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PATENT

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**CIRCUITS AND METHODS FOR DEBUGGING AN EMBEDDED PROCESSOR
AND SYSTEMS USING THE SAME**

ABSTRACT OF THE DISCLOSURE

[00382] A debugging subsystem for testing a system-on-a-chip includes an embedded processor and memory and includes at least one debugging subblock monitors a bus between the processor and the memory to detect selected triggering events, counts the number of triggering events detected and when the number of triggering events reaches a predetermined threshold, generates a debugging signal.

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